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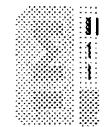
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3	145	714/756.ccls.	USPAT; EPO; JPO; DERWENT	2004/10/26 11:26
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9	2	decod\$3 same (ECC or (error adj2 correct\$4) or FEC or (forward adj2 error adj2 correct\$*)) same (syndrome near2 generat\$4) same (scrambl\$4 or de-scrambl\$4)	USPAT; EPO; JPO; DERWENT	2004/10/26 11:30
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11	17	(decod\$3 same (ECC or (error adj2 correct\$4) or FEC or (forward adj2 error adj2 correct\$*)) same (syndrome near2 generat\$4)) and (scrambl\$4 or de-scrambl\$4) and parity	USPAT; EPO; JPO; DERWENT	2004/10/26 11:31
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Ashikhmin, A.; Kramer, G.; tenBrink, S.;
Information Theory, IEEE Transactions on , Volume: 50 , Issue: 11 , Nov. 2000
Pages:2657 - 2673

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2 The Simplex Codes and Other Even-Weight Binary Linear Codes for Error Correction

Helleseth, T.; Klove, T.; Levenshtein, V.I.;
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3 Subspace-Based Error and Erasure Correction with DFT Codes for Wireless Channels

Rath, G.; Guillemot, C.;
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Pages:3241 - 3252

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4 Soft-Input Reconstruction of Binary Transmitted Quantized Overcomplete Expansions

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Fernandez, M.; Soriano, M.;

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7 Analyzing low-density parity-check codes on partial response channel with erasures using density evolution

Tan, W.; Cruz, J.R.;

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8 Highly accurate measurement of sheet thickness: the planar error correction

Tojo, F.; Hirakawa, S.; Toyoda, T.; Yamagata, K.; Omura, A.; Maeji, Y.; Itoh, A.
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9 Stray field of a pulse septum induced by eddy currents

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11 Interpolation-free coregistration and phase-correction of airborne LiDAR images

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12 Residential microwave oven interference on Bluetooth data performance

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13 Design of stable IIR digital filter based on least P-power error crite

Chien-Cheng Tseng;

Circuits and Systems I: Regular Papers, IEEE Transactions on [see also Circuit Systems I: Fundamental Theory and Applications, IEEE Transactions on] , Vol 51 , Issue: 9 , Sept. 2004
Pages:1879 - 1888

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14 High-throughput error correcting space-time block codes

Martin, P.A.; Taylor, D.P.;

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15 Design of flexible-length S-random interleaver for turbo codes

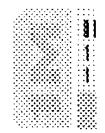
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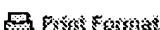
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1 Area efficient parallel decoder architecture for long BCH codes

Yanni Chen; Parhi, K.K.;

Acoustics, Speech, and Signal Processing, 2004. Proceedings. (ICASSP '04). II International Conference on , Volume: 5 , 17-21 May 2004

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2 FPGA based realization of a reduced complexity high speed decoder error correction

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